

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Tae-Woo Jung, et al.

Application No.: 10/750,021

Filed: December 30, 2003

**For: METHOD FOR FABRICATING
SEMICONDUCTOR DEVICE HAVING TRENCH
TYPE DEVICE ISOLATION LAYER**

Art Group: 2823

Examiner: Quovaunda Jefferson

AMENDMENT AND RESPONSE TO OFFICE ACTION

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir or Madam:

In response to the Office Action dated September 7, 2006, Applicants submit the following amendments and remarks: